

1 SYSTEM AND METHODS FOR 2-TAP / 3-TAP FLICKER FILTERING

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3 ABSTRACT OF THE DISCLOSURE

4 A device performs both 2-tap and 3-tap flicker filtering of  
5 non-interlaced lines of computer graphics data to form interlaced  
6 lines. The device includes a data packer, a data unpacker, and a  
7 filter circuit. The filter circuit combines lines that it  
8 receives to form filtered lines. The data packer writes the  
9 filtered lines to line buffers while the data unpacker reads the  
10 lines stored in the line buffers. The read lines are either sent  
11 to the filter circuit for further filtering or are outputted to  
12 be displayed as interlaced lines. Both 2-tap and 3-tap flicker  
13 filtering can be accomplished by varying the order and/or number  
14 of read, write, and filter operations.